



FIG 1

The diagram illustrates a radio receiver and transmitter system. The receiver path (left) includes an antenna (A) connected to a Duplex Unit (DUP) and a Frequency Splitter. The receiver path consists of LNA1, BP1, V1, M1, TP1, RI, V3, AD, TP2, and DM1. The transmitter path (right) includes DM2, DA, TP3, M3, X, M3', TP3', AGC1, BP3, TI, V4, M2, BP4, AGC2, BP5, PA, and a final antenna (A). The system includes two PLLs (PLL1, PLL1'), two VCOs (VC01, VC02), an Oscillator, and various filters (BP1, BP2, BP3, BP4, BP5), amplifiers (V1, V2, V3, V4, V5, V6), and mixers (M1, M1', M2, M3, M3'). A DC block is also present.